

Fig. 1

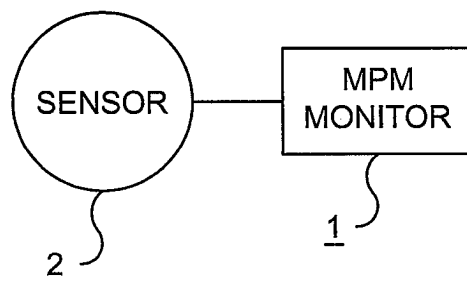


Fig. 2

SINGLE THERMISTOR METHOD

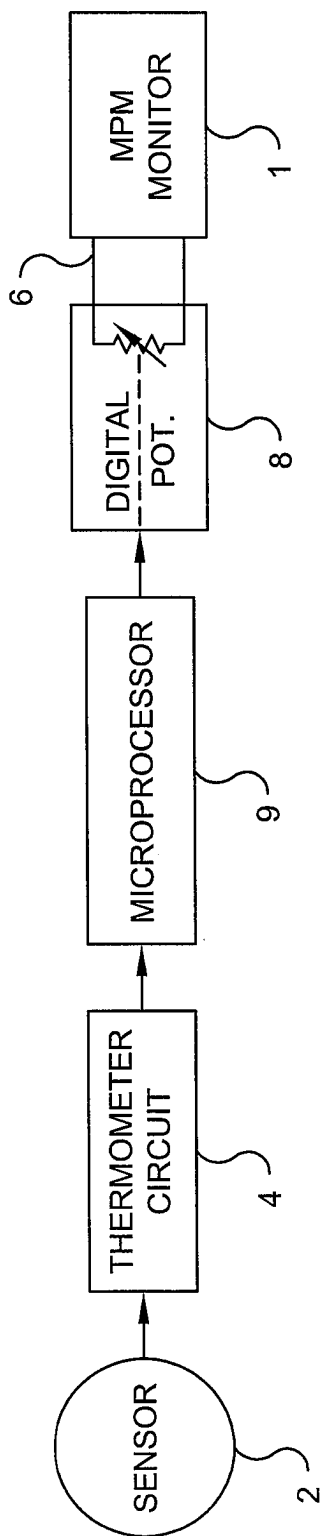


Fig. 3

DUAL THERMISTOR METHOD

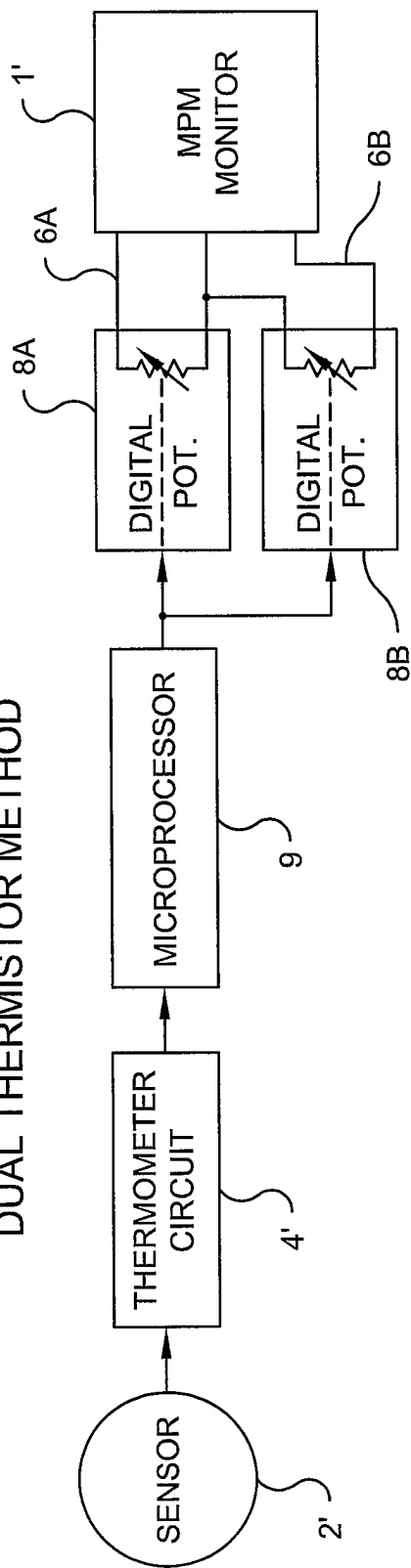


Fig. 4

FET METHOD

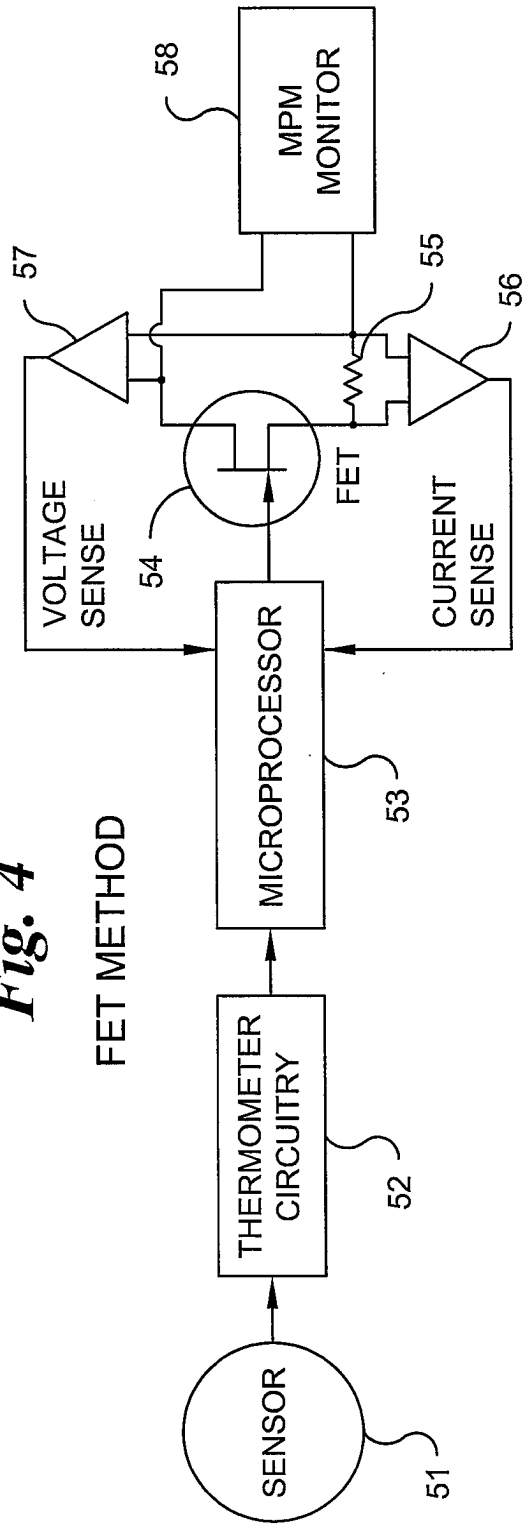


Fig. 5

PHOTOCELL METHOD

